

J = chip has serial jtag connection to Service Processor HW

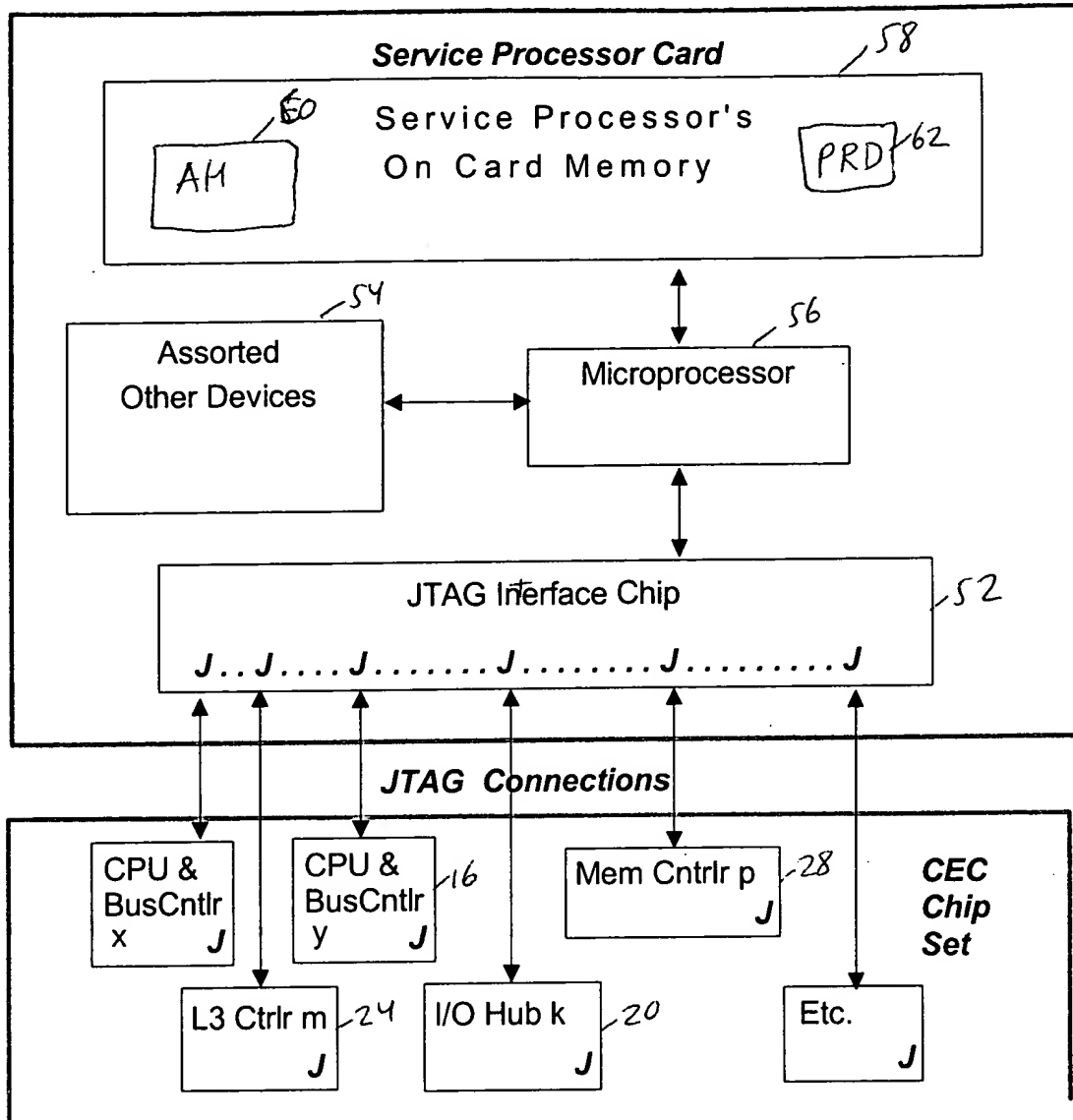
□ dotted box indicates remote chip or device inaccessible to SP via jtag

◄► dotted line indicates connections to and between remote chips & devices

Figure 1

218  
av5970010114US1

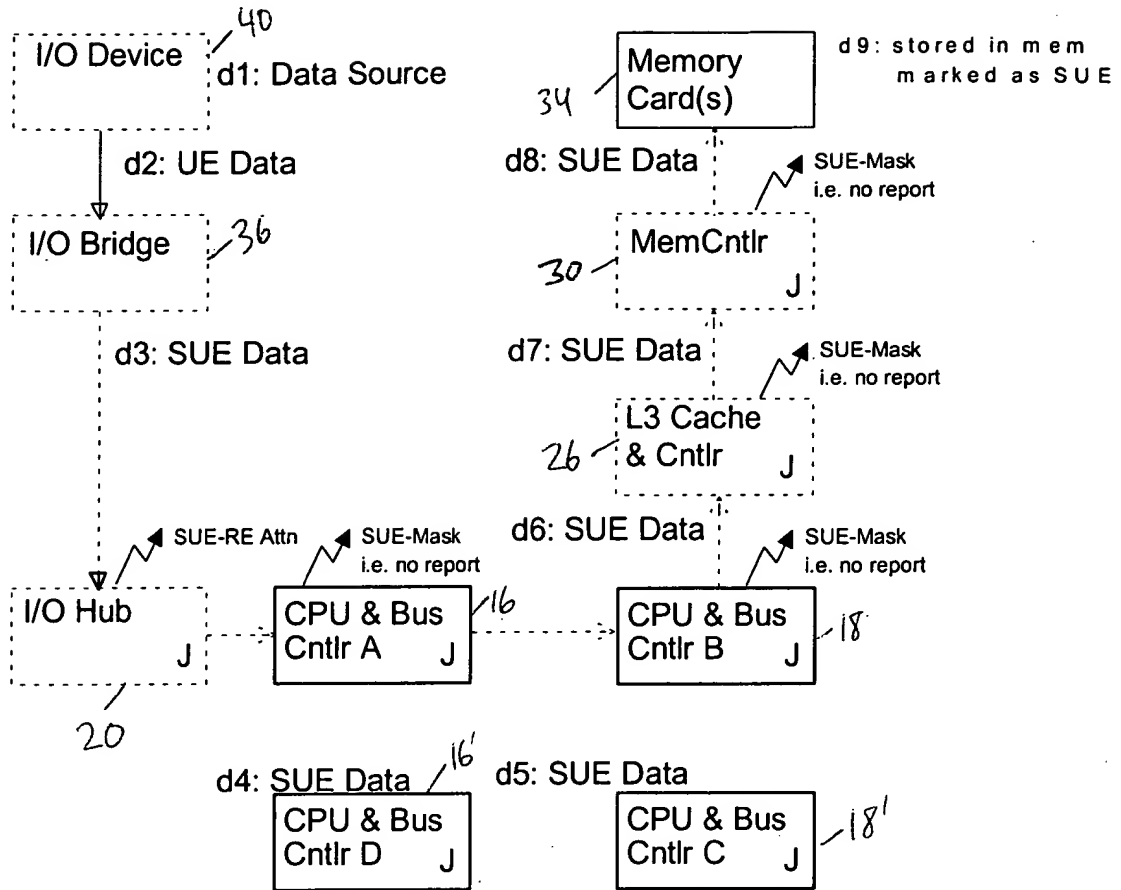
50



**J = JTAG serial interface between Service Processor & chips in CEC**

Figure 2

3/8  
a45920010114US)



J = chip has serial jtag connection to Service Processor HW  
 [dotted box] dotted box indicates remote chip or device inaccessible to SP via jtag  
 ◄-► dotted line indicates connections to and between remote chips & devices

Figure 3A

418  
av59200,0114W51

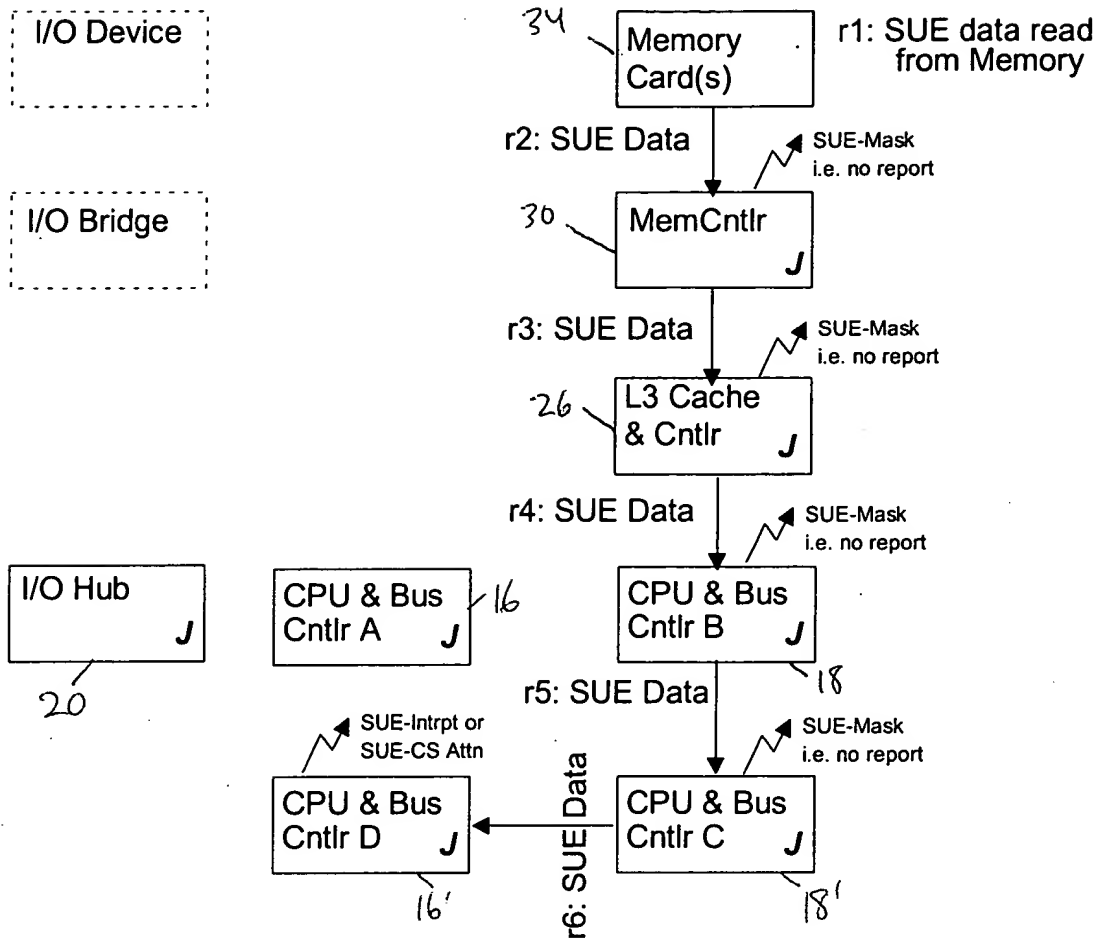


Figure 3B

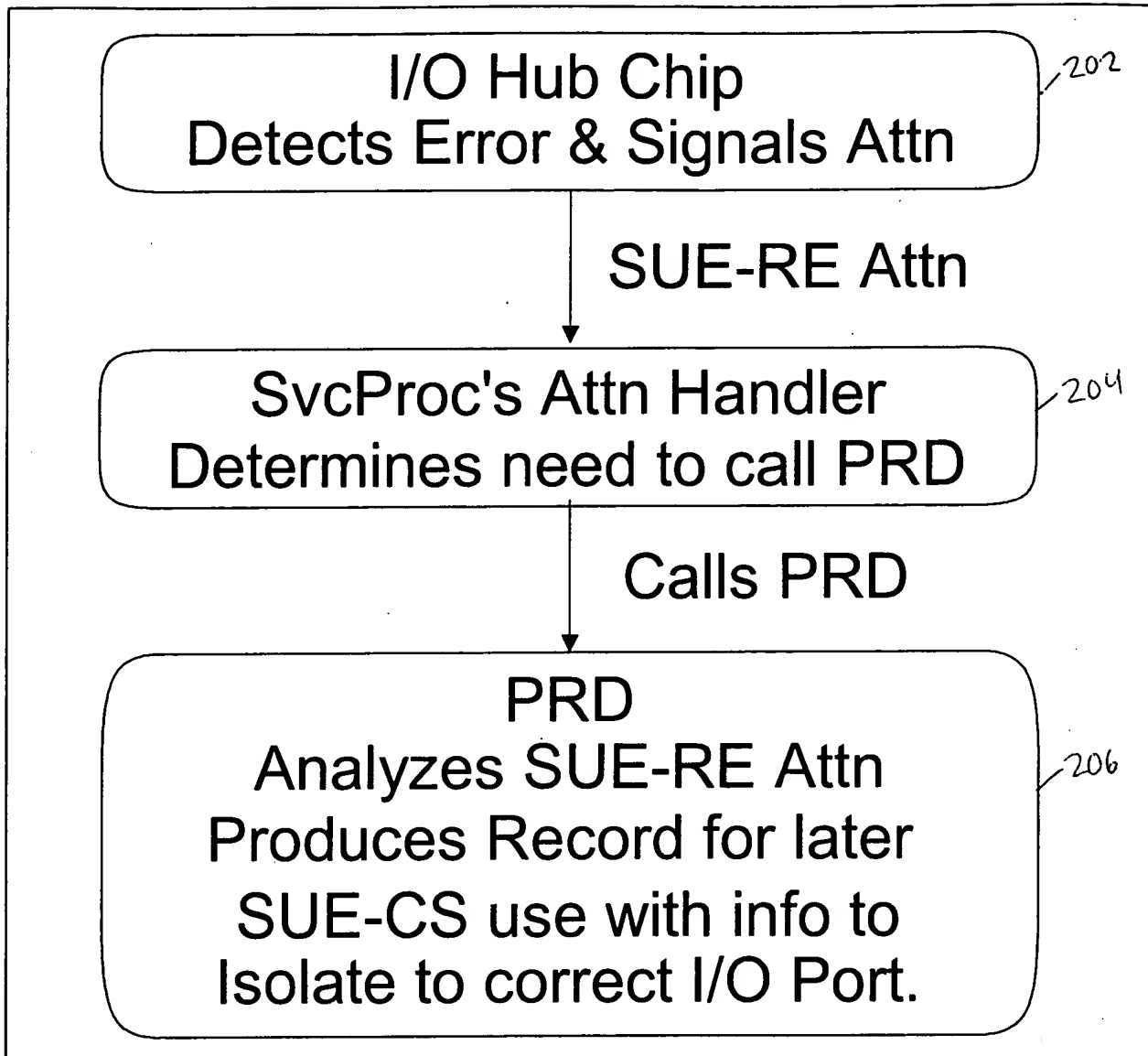


Figure 4

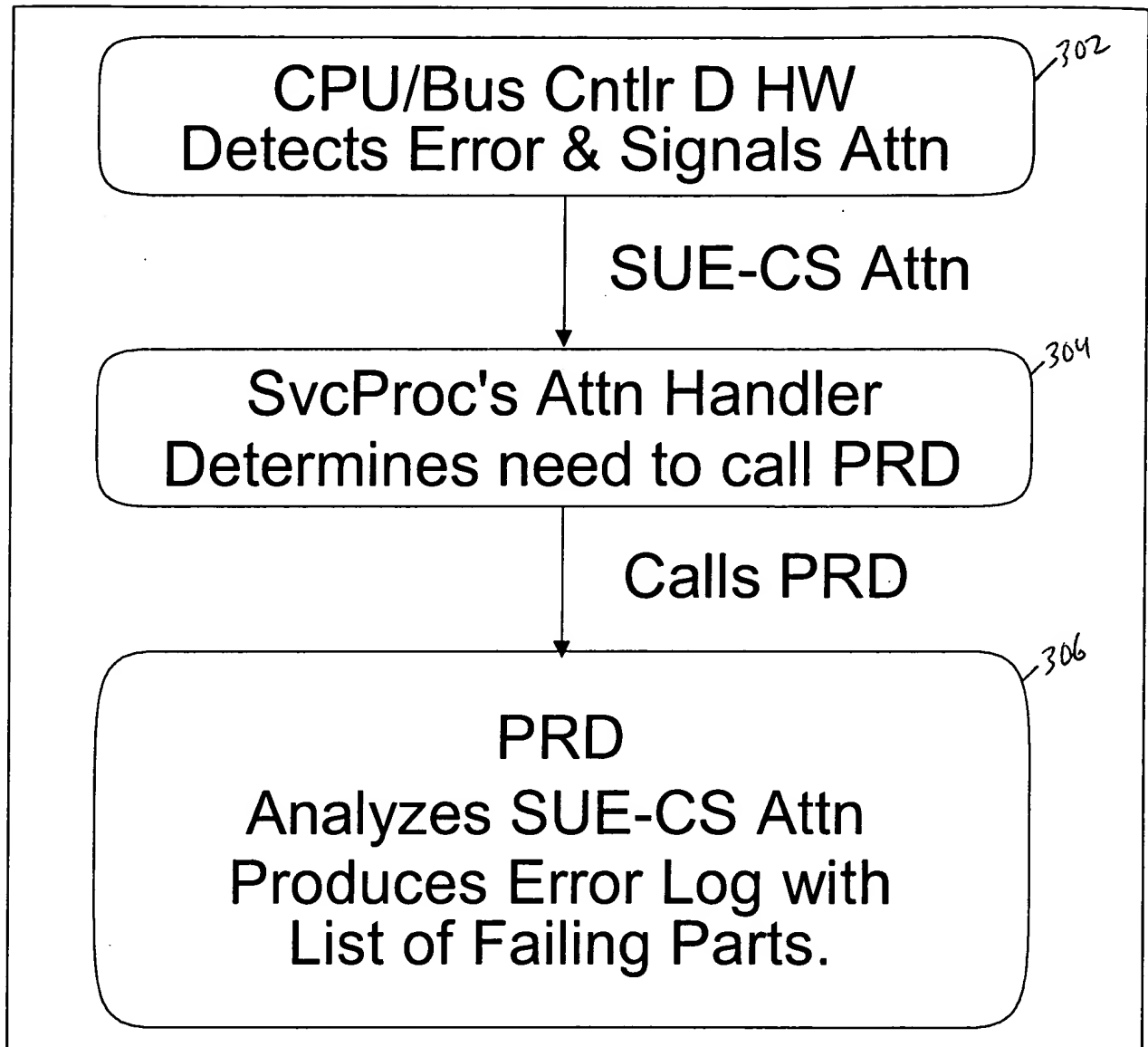


Figure 5

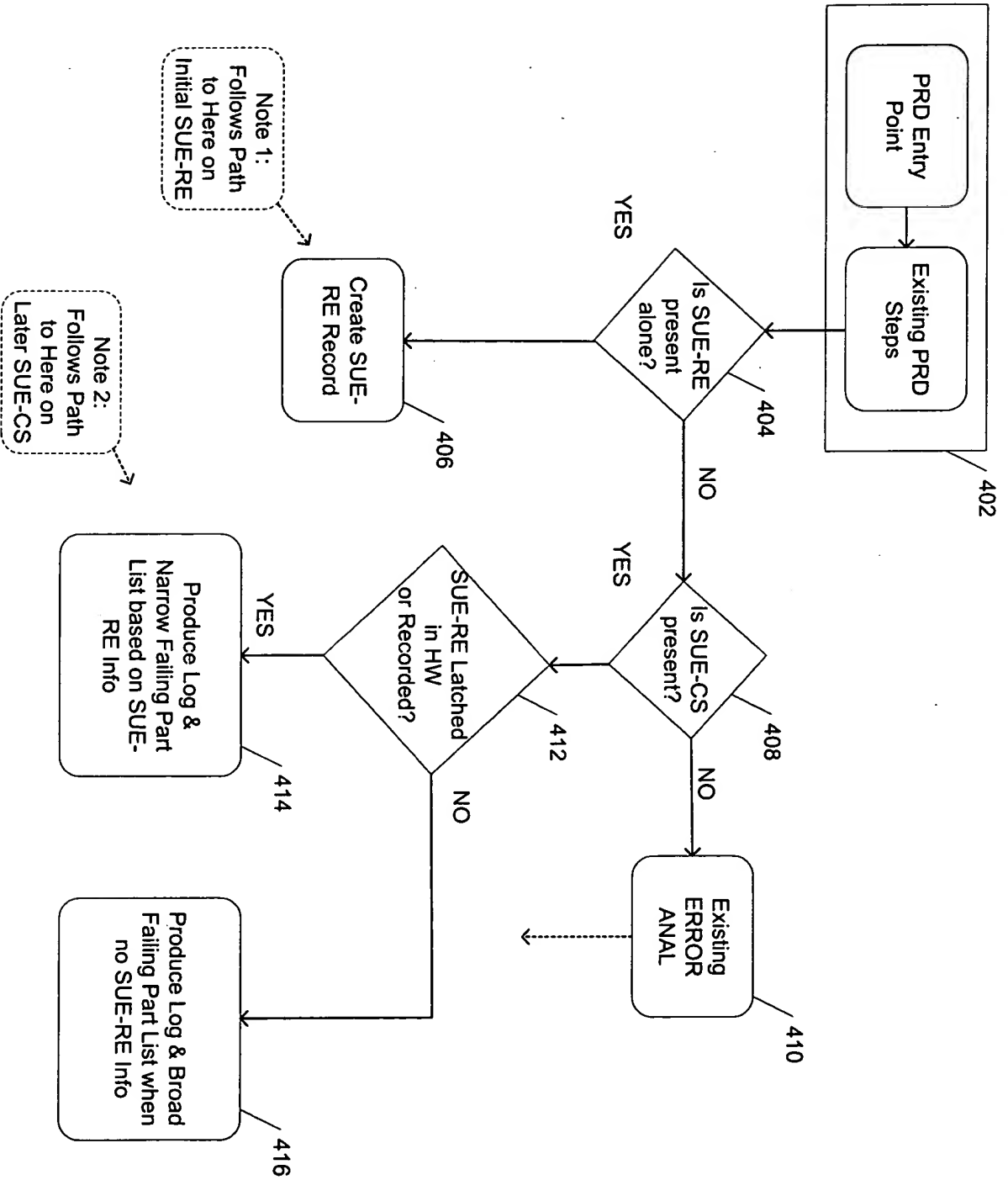


Figure 6

39820450 000000

15411010025520  
817

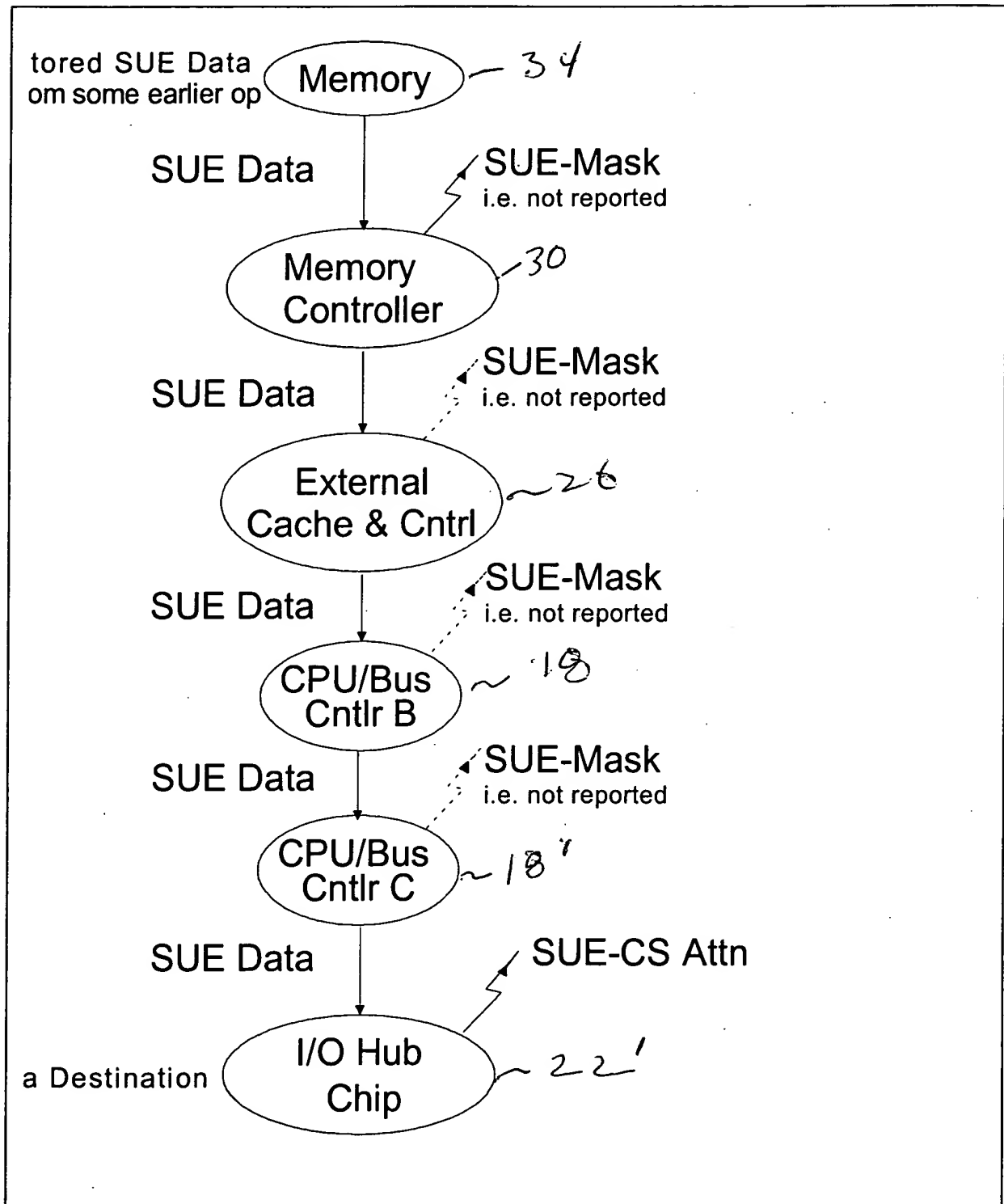


Figure 7